

Fig. 1

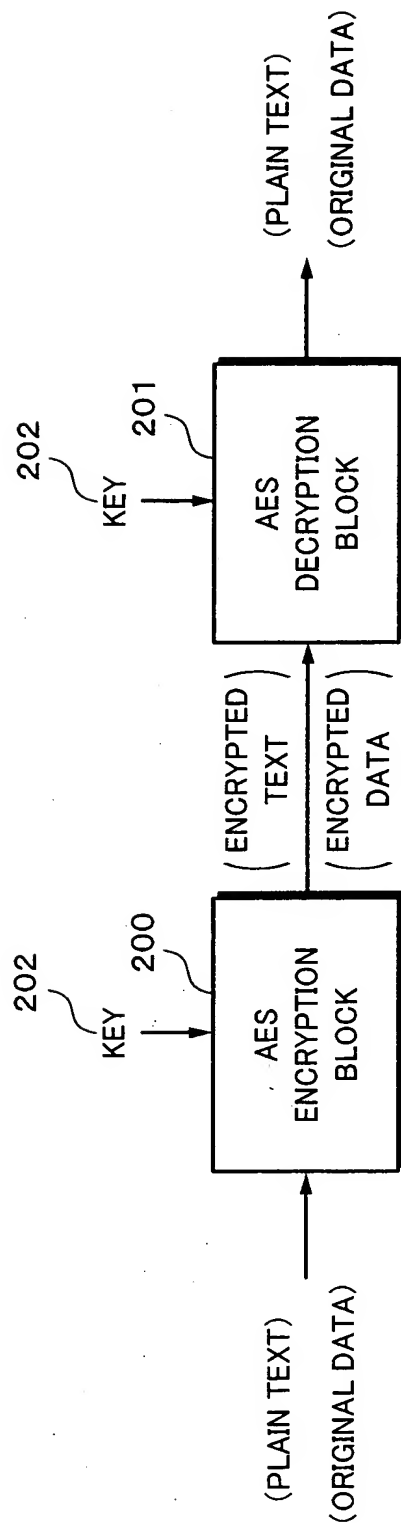


Fig. 2

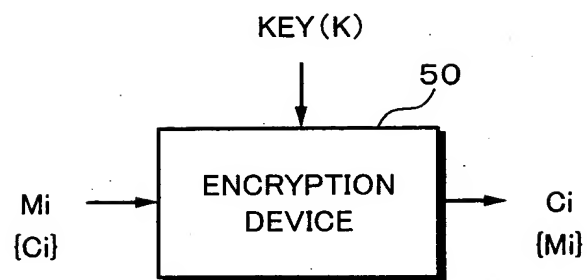


Fig. 3A

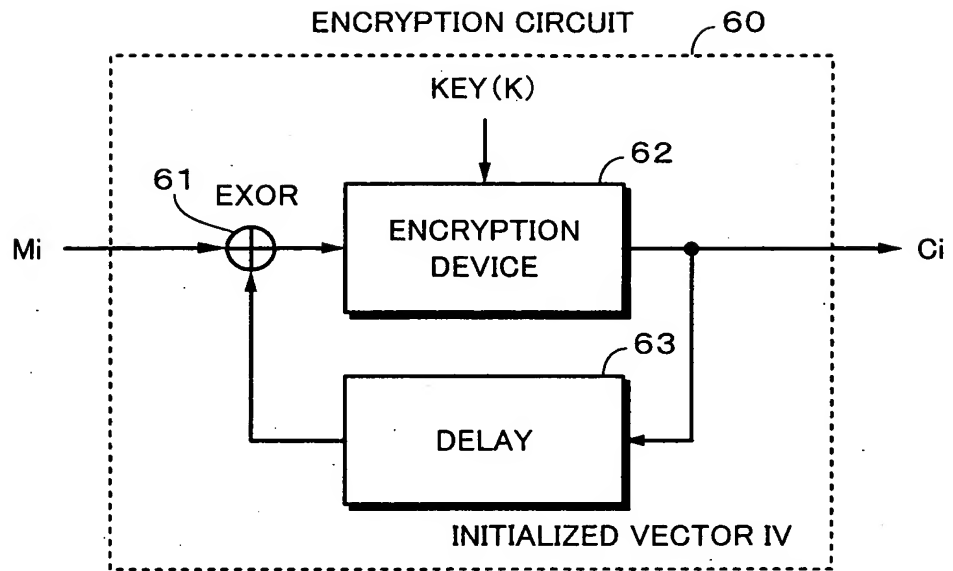


Fig. 3B

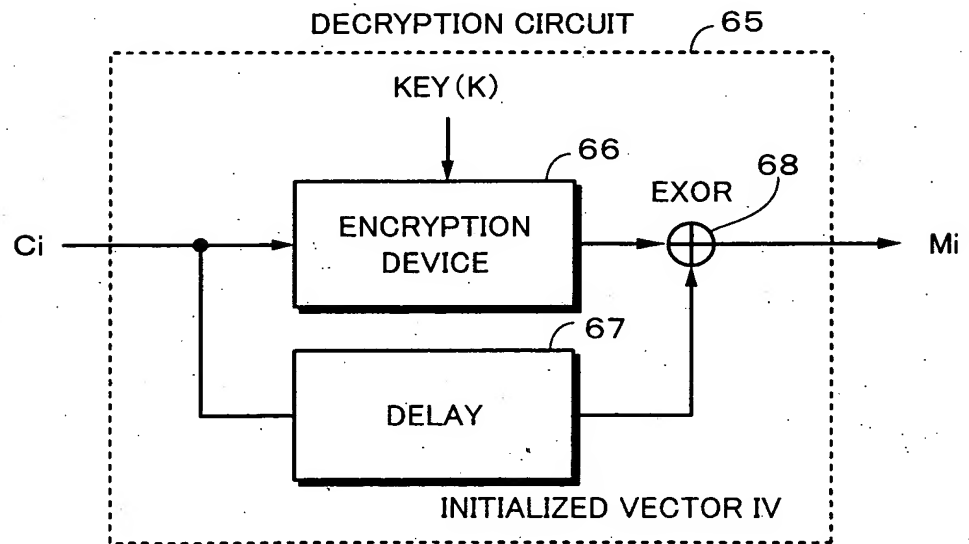


Fig. 4A

GENERATION
OF PSEUDO
RANDOM
SEQUENCE

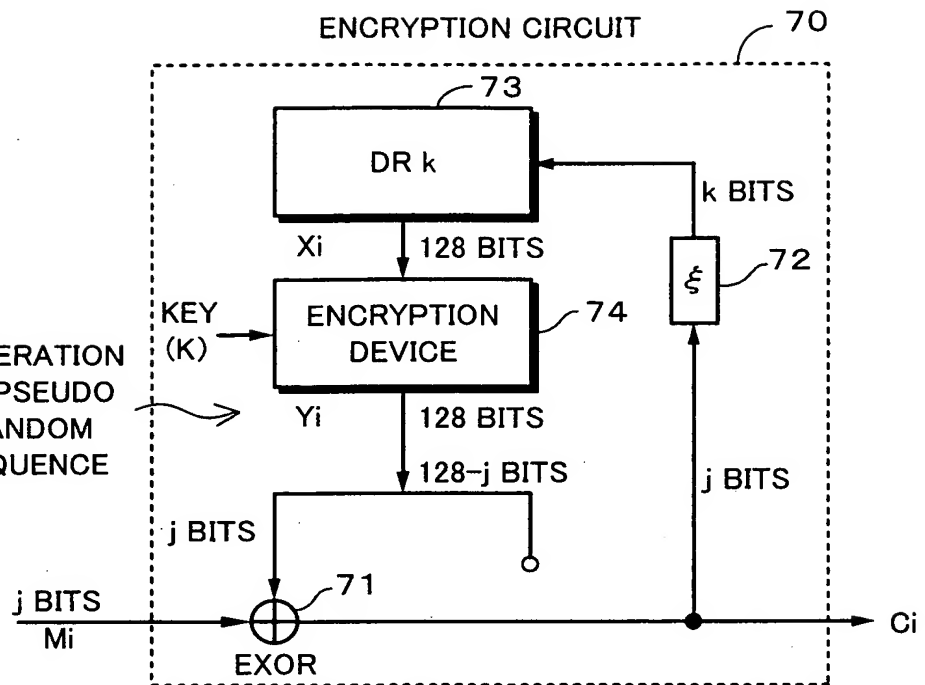


Fig. 4B

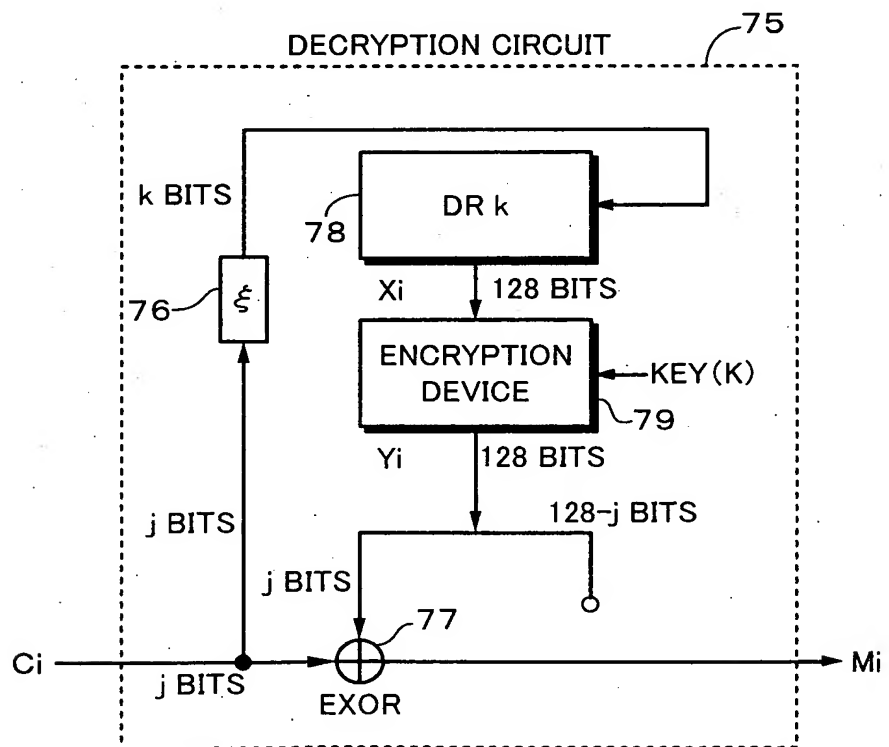


Fig. 5A

GENERATION
OF PSEUDO
RANDOM
SEQUENCE

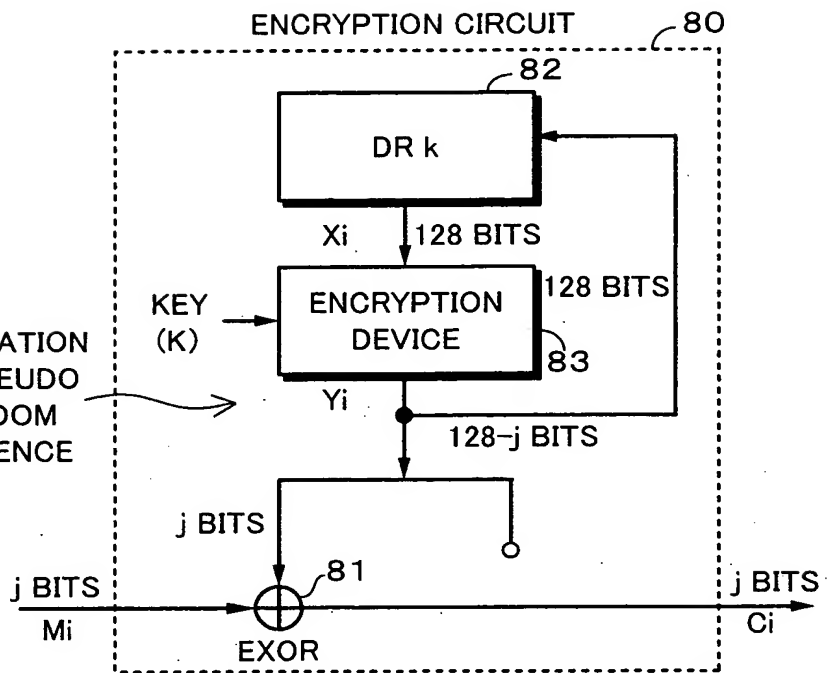


Fig. 5B

GENERATION
OF PSEUDO
RANDOM
SEQUENCE

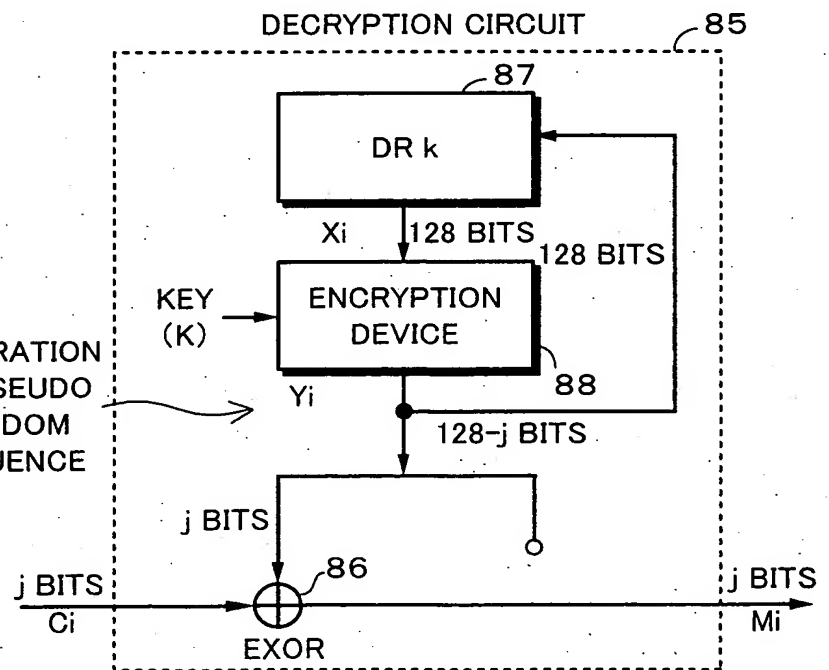


Fig. 6A

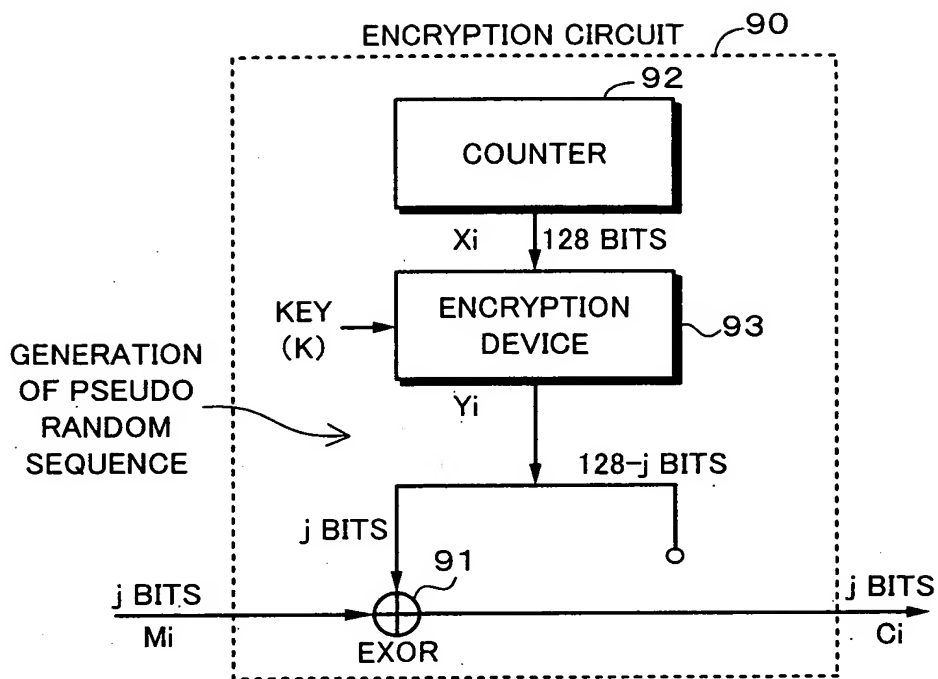


Fig. 6B

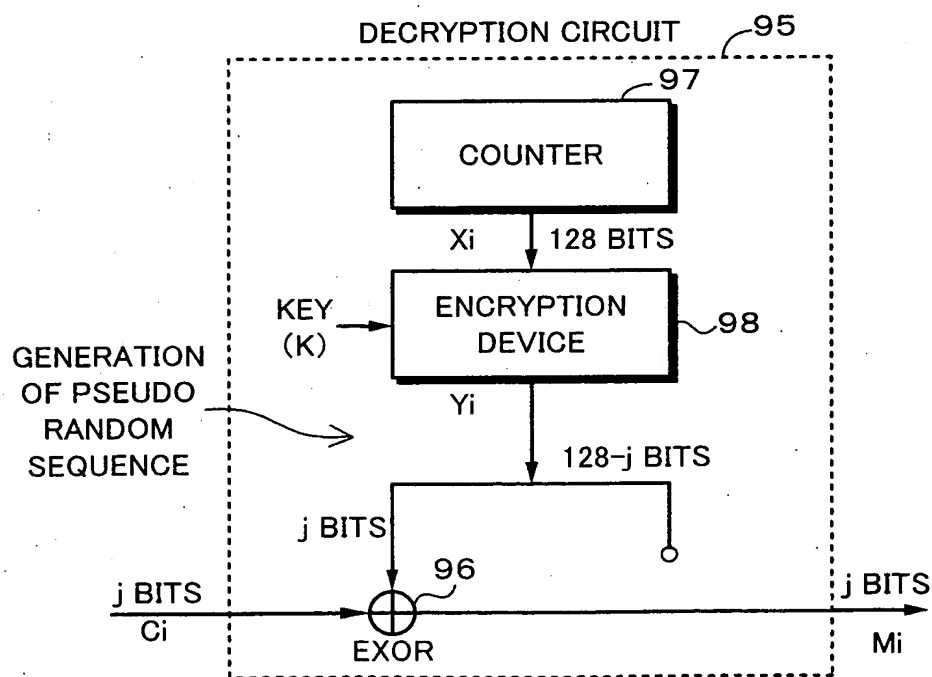


Fig. 7

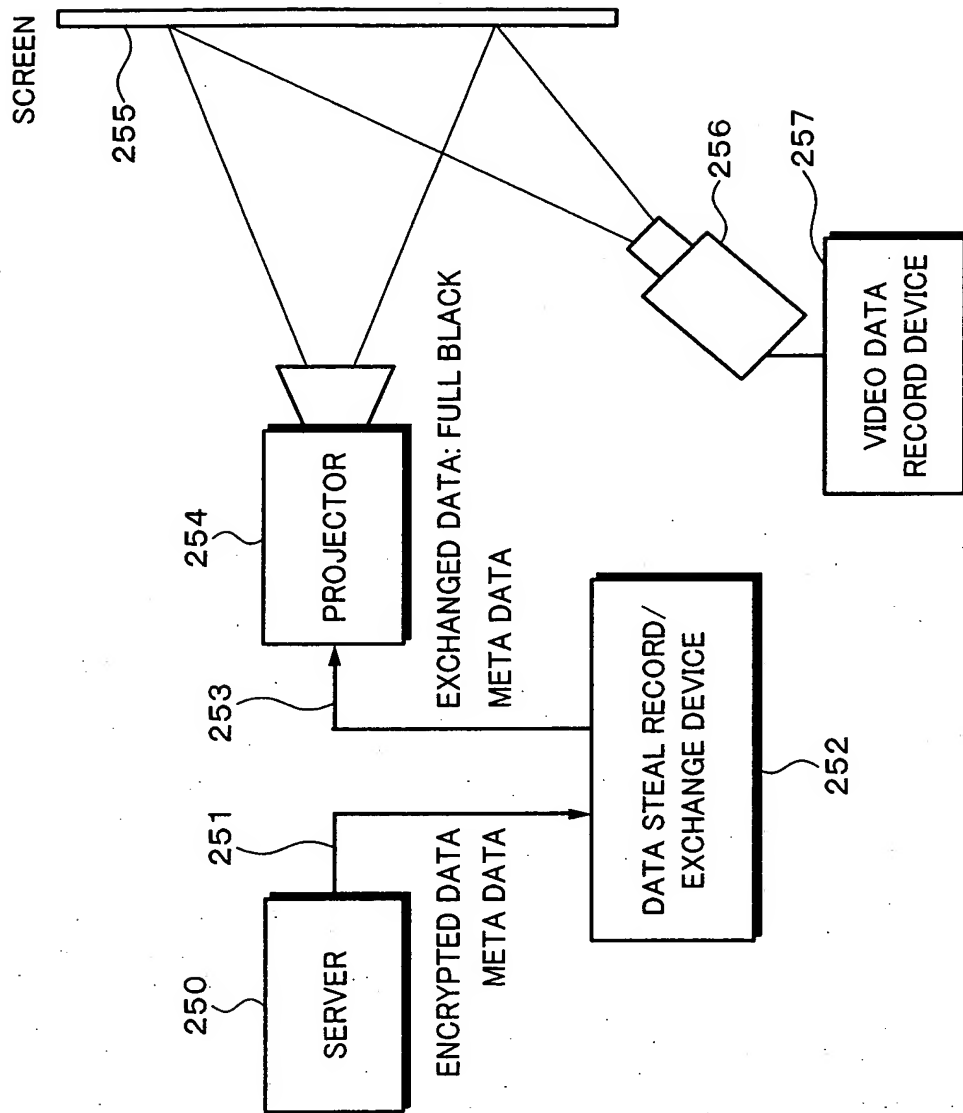


Fig. 8

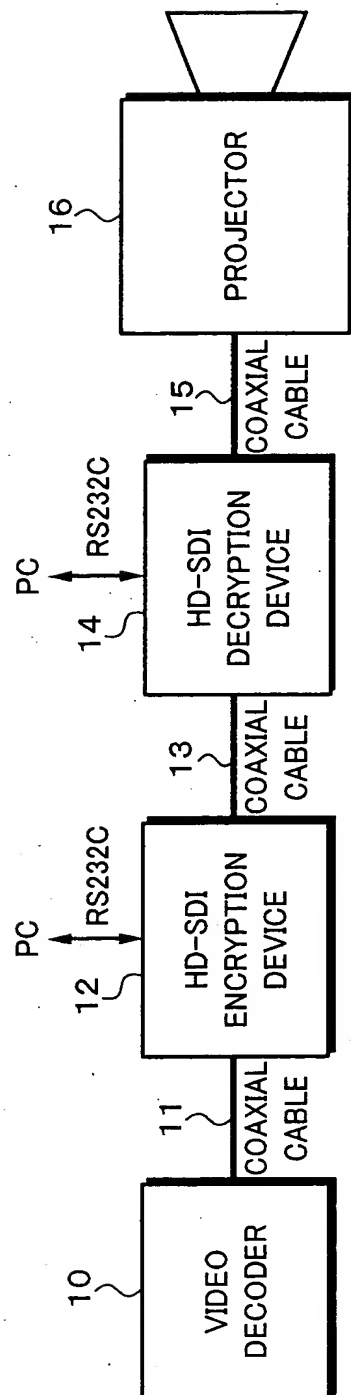


Fig. 9

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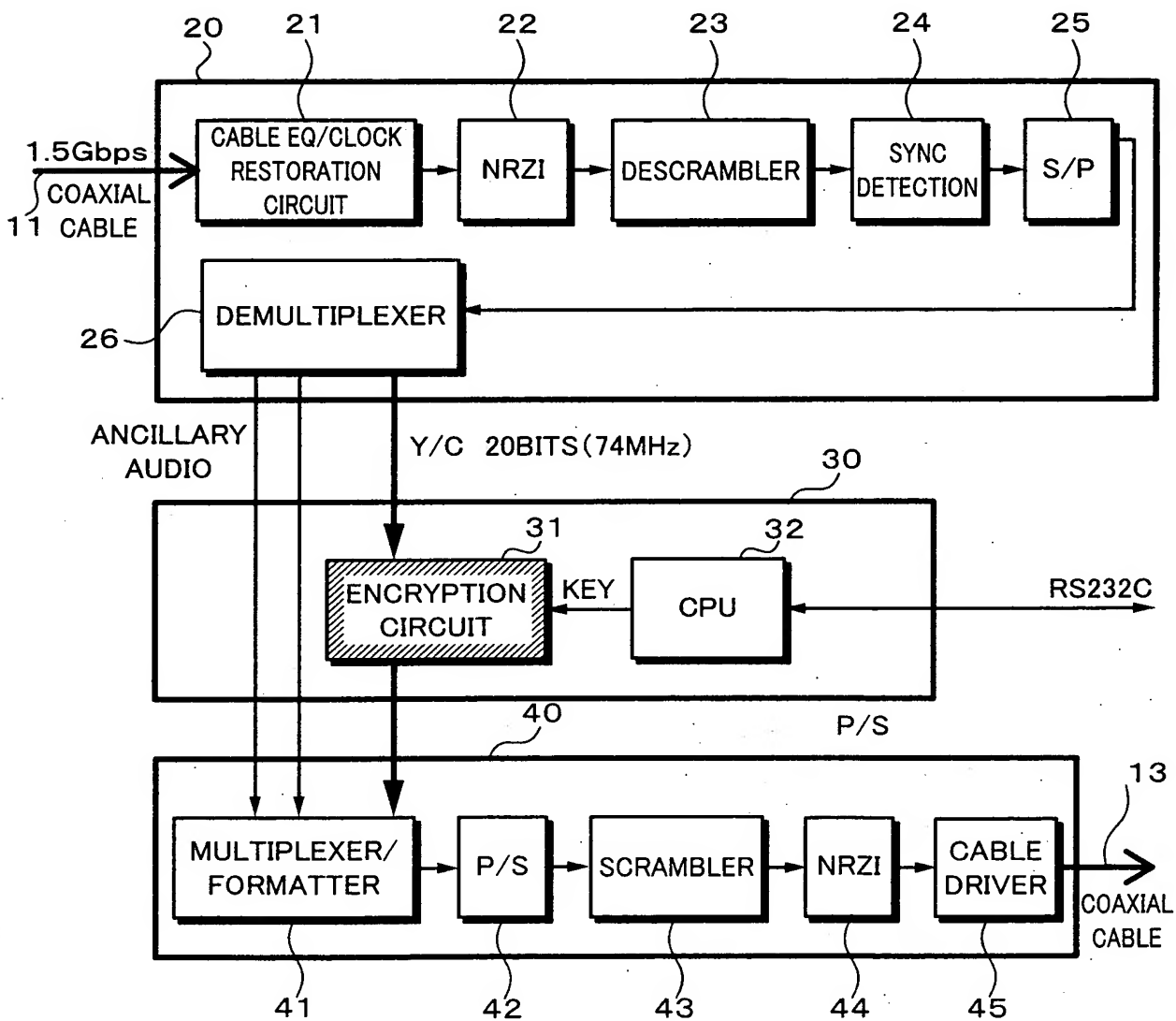


Fig. 10

31

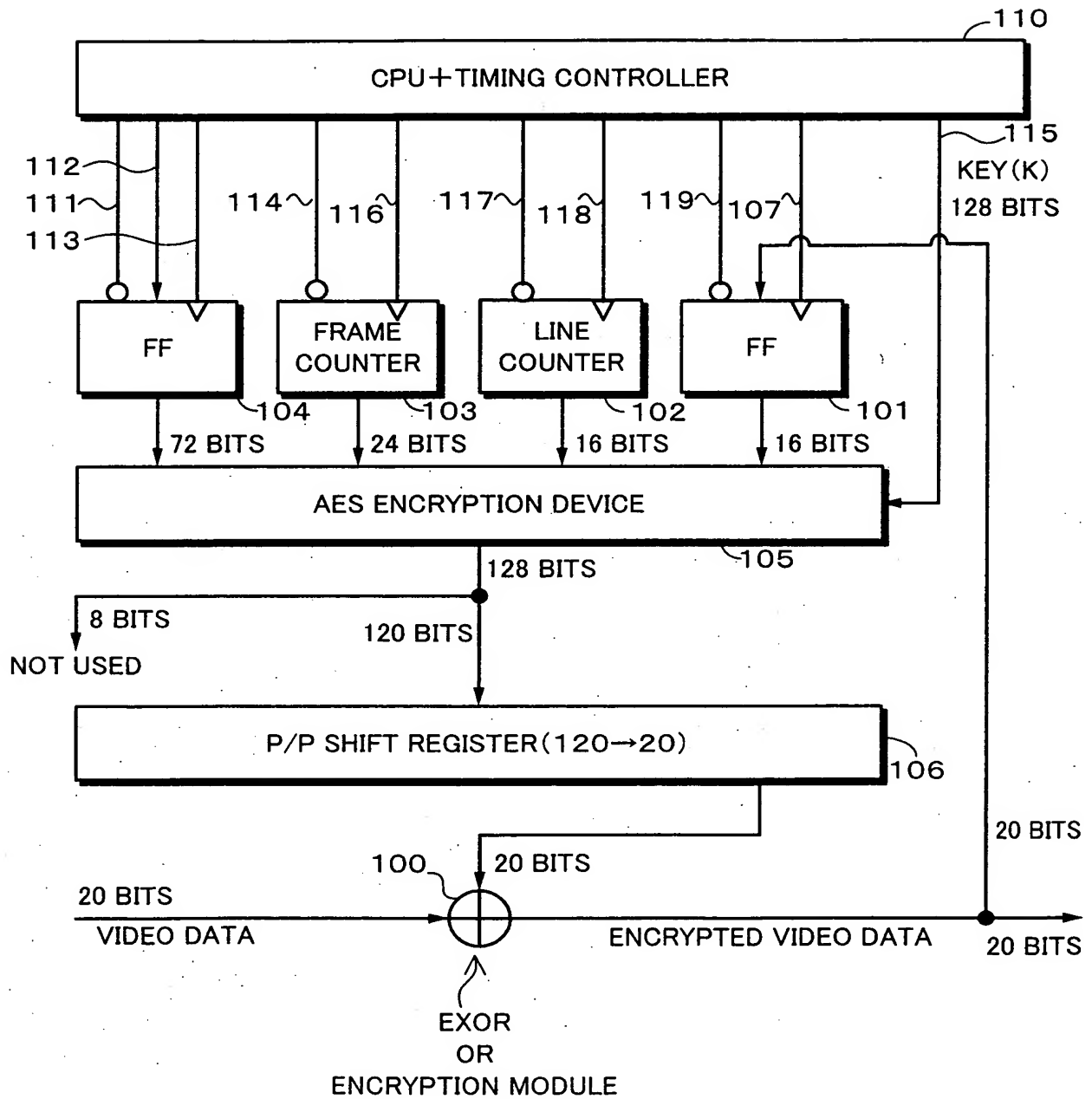
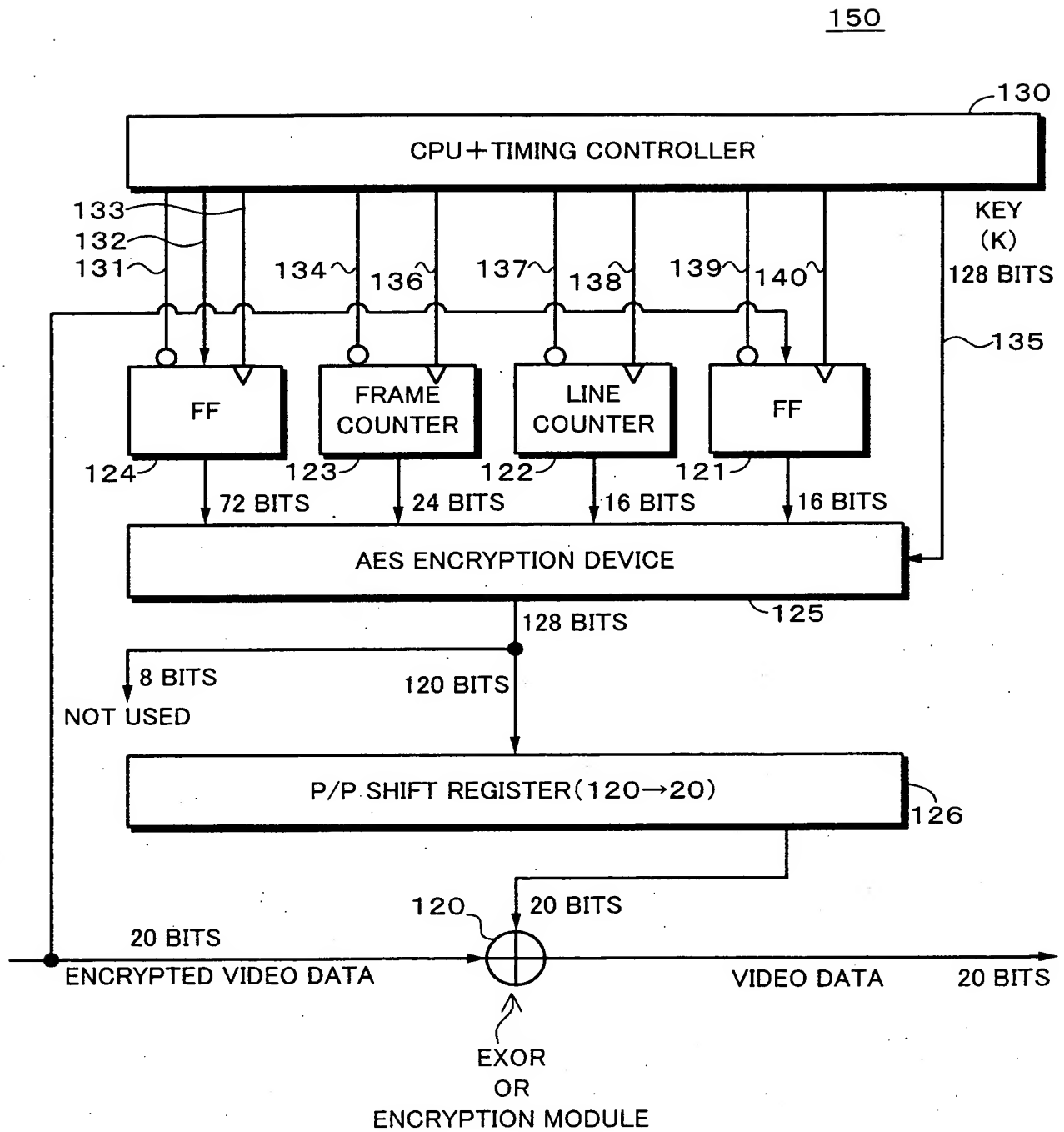


Fig. 11



DESCRIPTION OF REFERENCE NUMERALS

10	VIDEO DECODER
12	HD-SDI ENCRYPTION DEVICE
13	COAXIAL CABLE
14	HD-SDI DECRYPTION DEVICE
16	PROJECTOR
20	HD-SDI SERIAL/PARALLEL CONVERSION CIRCUIT BLOCK
26	DEMULTIPLEXER
30	ENCRYPTION CIRCUIT BLOCK
31	ENCRYPTION CIRCUIT
32	CPU
40	HD-SDI PARALLEL/SERIAL CONVERSION CIRCUIT BLOCK
41	MULTIPLEXER/FORMATTER
50	DECRYPTION CIRCUIT
100	EXOR CIRCUIT
101	FF CIRCUIT
102	LINE COUNTER
103	FRAME COUNTER
104	FF CIRCUIT
105	AES ENCRYPTION DEVICE
106	P/P SHIFT REGISTER
110	CPU + TIMING CONTROLLER